



INNOVATION PUT TO THE TEST



FORMFACTOR

Advanced Wafer Probe Solutions

Overview – Citi Technology Conference

Richard DeLateur, Chief Financial Officer

Investor Checklist

Advanced Probe Cards Role in Semiconductor Industry

The Importance of:

Matrix Product Family

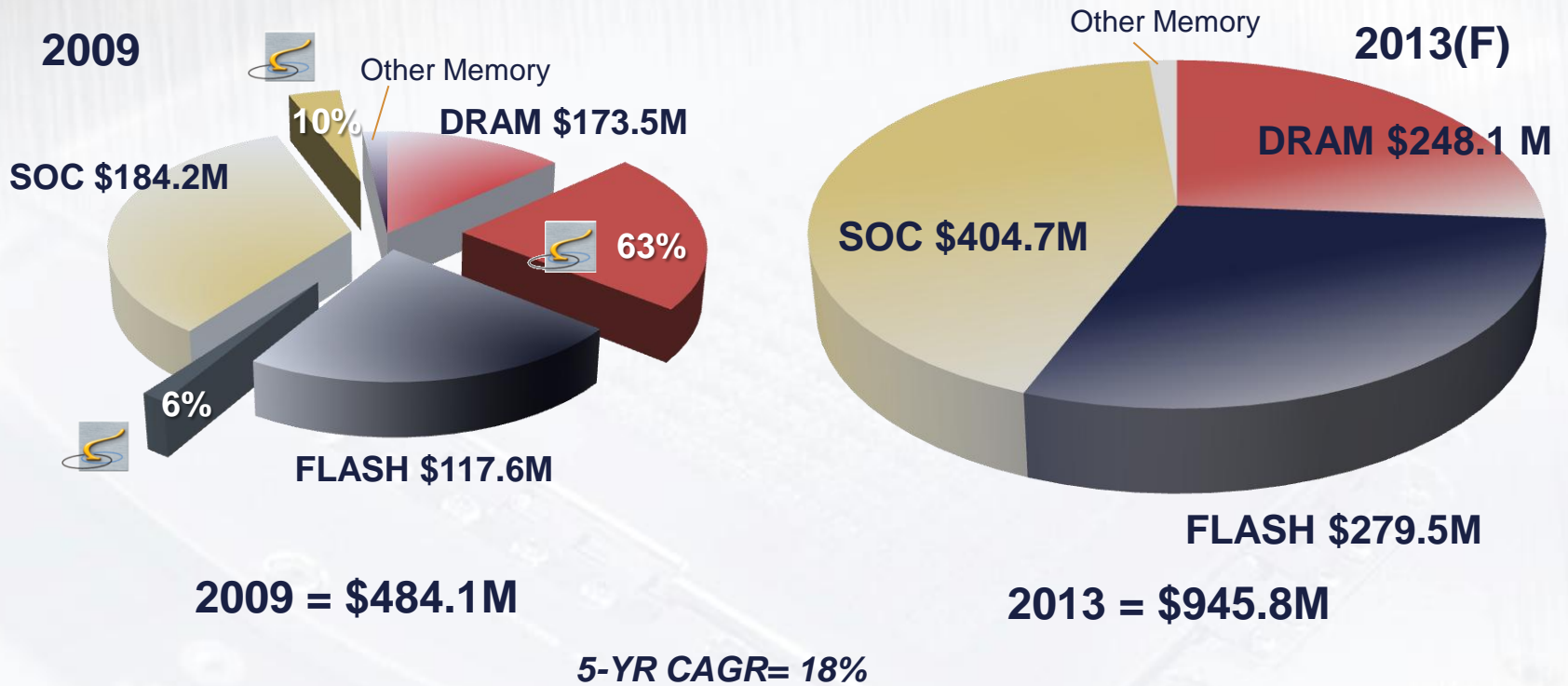
Advance Tester Resource Enhancement (ATRE)

3D vs 2D Springs

Monolithic Bonding

The Turnaround Plan at FormFactor

Advanced Probe Card Market



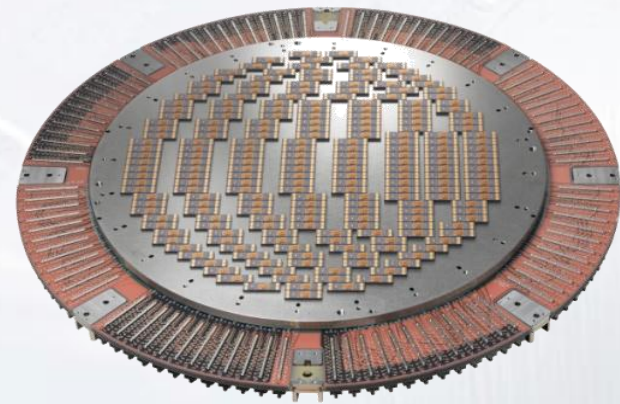
Source: VLSI Research, May 2010

SmartMatrix & TouchMatrix Architecture

- Best XY control
 - Best Z control
 - Zero Soak Time
 - Faster Installation
 - Faster Lead Times
 - Excellent CRES
 - Less Cleaning / Less Maintenance
- Smaller Pads, More die per wafer (DPW)
 - More DPW, Better Yield
 - Better Tester Utilization
 - More wafer starts
 - Faster time to test and to market
 - Better Yield
 - Better Tester Utilization



TouchMatrix



SmartMatrix

Turnaround Plan

How We Got Here:

Pricing & Cost

R&D Focus

Regionalization

What We Are Doing:

Driven by 50/30/20 Business Model

R&D Focus

- Design for Manufacturability
- Derivatives
- Probe Cards

Turnaround Plan

What we are doing (cont):

Factory Rationalization Post Regionalization

OPEX

Product Cost

- Transition & Mix
- Fixed Expenses
- Cost Reduction
- Supply Chain

Pricing